

PATENT NUMBER

**U.S. UTILITY Patent Application**

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APPLICATION NO. 09/531026	CONT/PRIOR D	CLASS 703	SUBCLASS 14	ART UNIT 2763	EXAMINER BRADA DAY
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**APPLICANTS:**

Sudheendra Hangal  
James O'Connor

## Time

### System and method for validating processor performance and functionality

PTO-2040  
12/89[illegible]

<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>	<b>DRAWINGS</b>		<b>CLAIMS ALLOWED</b>	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims
<input type="checkbox"/> The term of this patent subsequent to _____ (date) has been disclaimed.	_____ (Assistant Examiner) (Date)		<b>NOTICE OF ALLOWANCE MAILED</b>	
	<input type="checkbox"/> The term of this patent shall not extend beyond the expiration date of U.S Patent. No. _____  _____  _____  _____ (Primary Examiner) (Date)		<b>ISSUE FEE</b>	
Amount Due			Date Paid	
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